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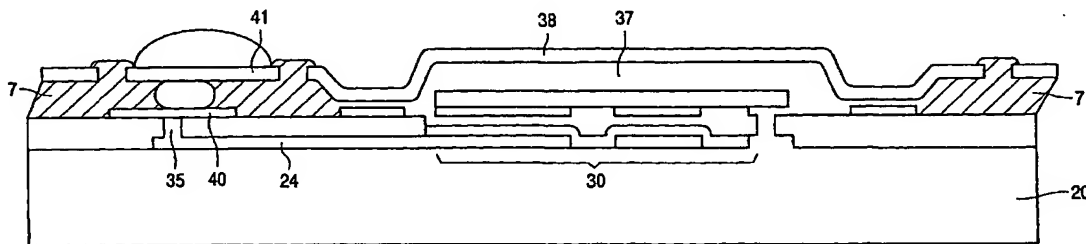
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(54) Title: METHOD OF MANUFACTURING AN ELECTRONIC DEVICE IN A CAVITY WITH A COVER



(57) Abstract: The electronic device (100) comprises an electrical element (30), for instance a MEMS capacitor or a BAW filter in a cavity (37) that is protected from the environment by a cover (38). The cover (38) is a patterned layer which is mechanically embedded in isolating material (7) present beside the cavity (37) and may further include contact pads (41). The device (100) may be suitably manufactured from an accurately folded foil including a patterned layer and a sacrifice layer. After applying the foil to the cavity (37) the isolating material (7) is provided and the sacrifice layer is removed. The patterned layer, or part thereof, stays behind and forms the cover (38).

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## INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER  
IPC 7 B81B7/00 H03H9/05

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)  
IPC 7 B81B H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)  
EPO-Internal, WPI Data, PAJ

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	EP 0 828 346 A (HARRIS CORP) 11 March 1998 (1998-03-11) column 4, line 1 -column 4, line 13; figure 3	1,8
A	US 5 738 797 A (BELKE JR ROBERT E ET AL) 14 April 1998 (1998-04-14) column 3, line 31 -column 3, line 52; figures 1G,1H	2

☐ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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